

The diagram illustrates a multi-channel receiver and transmitter system. At the top, four antennas are connected to a central **DUPLEXER** (107) which has **RX** and **TX** ports. The **RX** path from the duplexer splits into four parallel channels, each passing through a **4 x RF RECEIVER MODULES** (205), an **ADC** (209), a **DOWN CONVERTER** (213), and **TIMESLOT PROCESSORS** (217) before being connected to a central **HOST DSP** (231). The **TX** path from the duplexer connects to a central **RF/TIMING CONTROLLER** (233), which then connects to **RF TRANSMIT MODULES** (245) and a **TRANSMIT CONTROLLER/MODULATOR** (237) before reaching the **HOST DSP** (231). The **HOST DSP** (231) is also connected to **HIGHER LEVEL PROCESSING**. Various signal paths are labeled with the number 4, indicating multiple parallel connections.

## HIGHER LEVEL PROCESSING

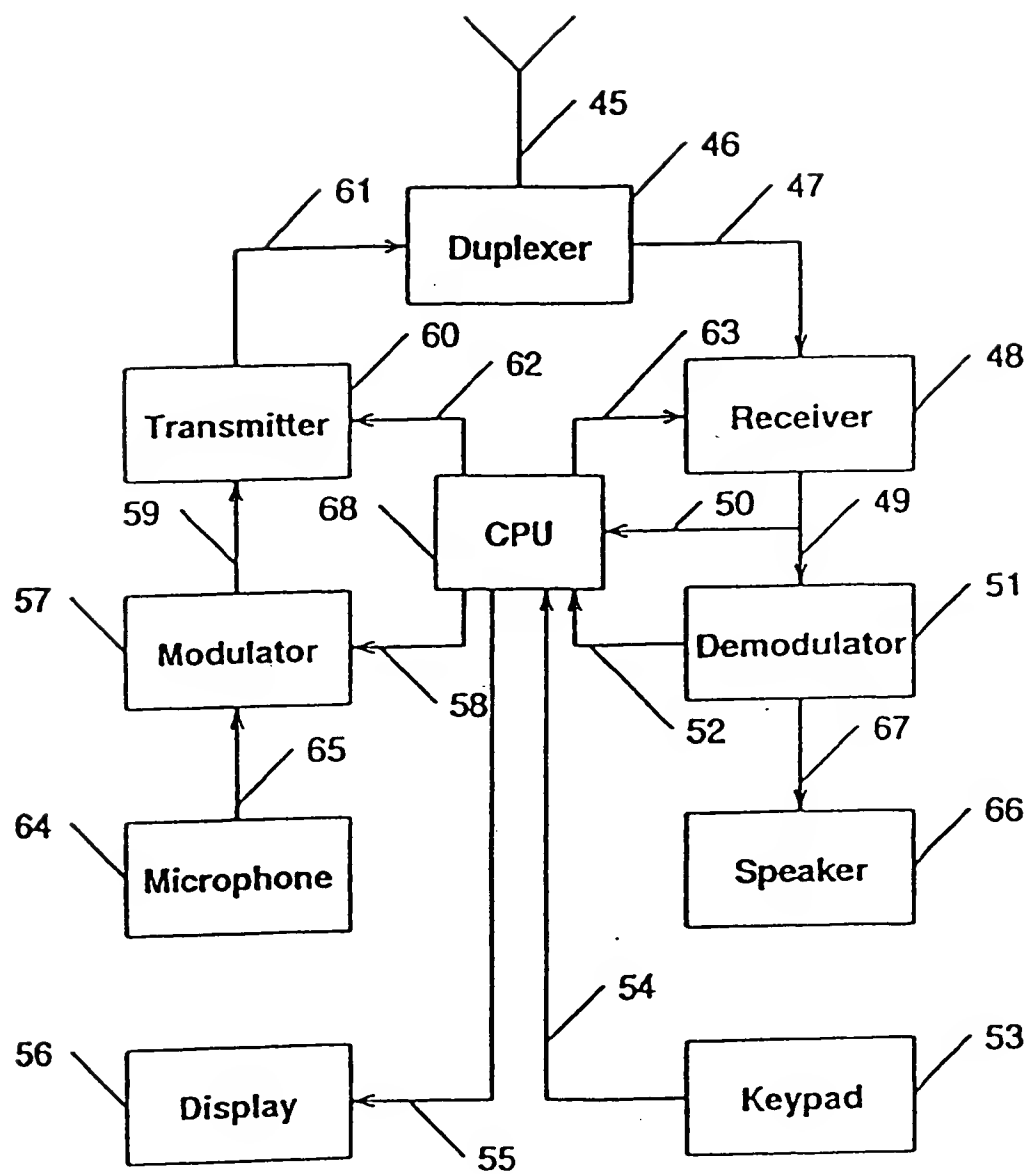


FIG. 2

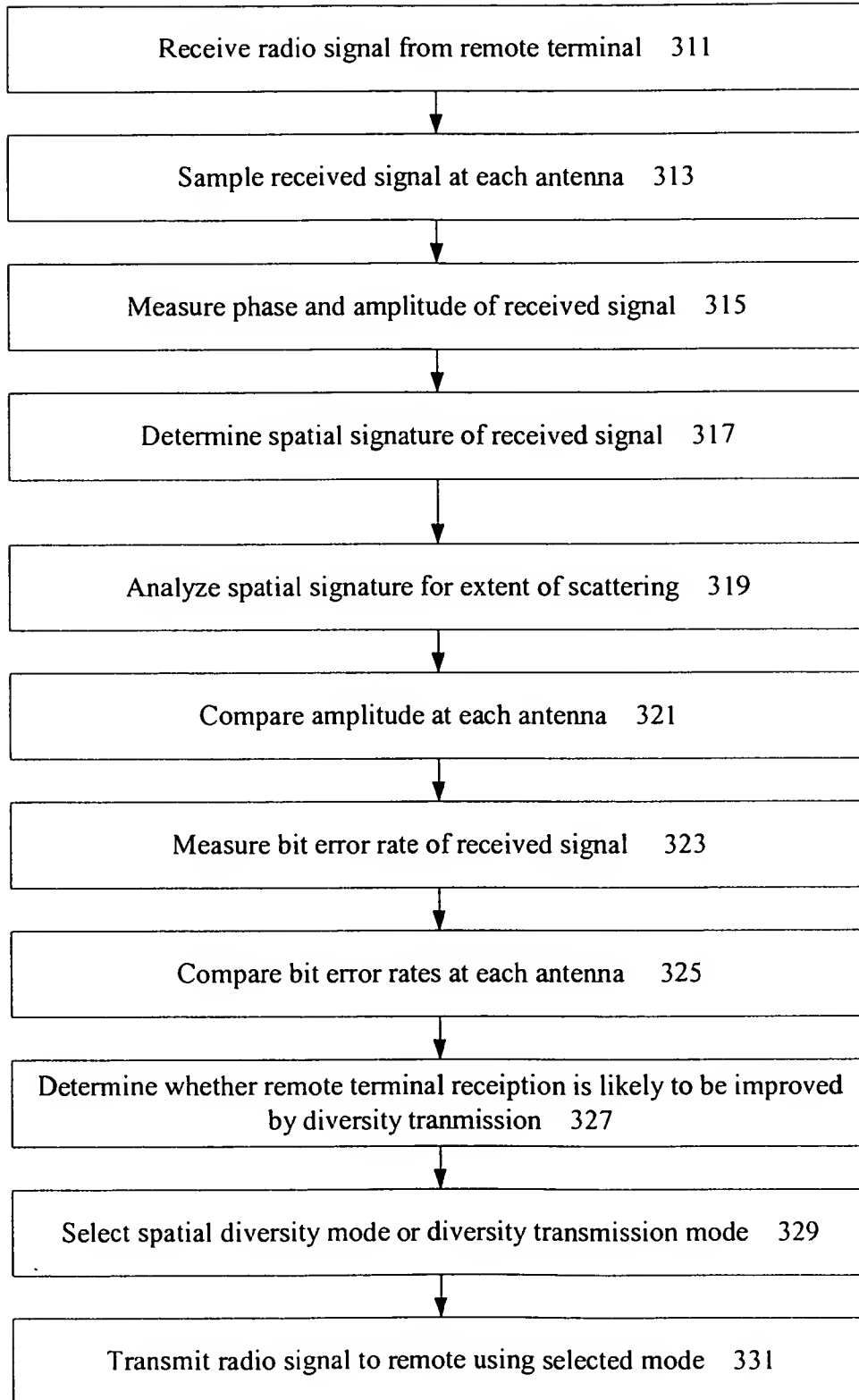


Figure 3